

SECTION III. OPERATION

15.3.1 INTRODUCTION

The operation of the PSD is transparent to the user. Message management by the PSD, including acknowledgment and retransmission, occurs automatically with no operator intervention. The PSD also creates necessary queues to process the message traffic.

15.3.2 CONTROLS AND INDICATORS

The operator has no direct interface with or control over the PSD. All operator functions associated with the PSD, including configuring the RTA and ABT, viewing the PSD memory board self-test status, and using the PSD DIAG function are performed using the operator interface device (OID) display pages. The memory board in location A4 is the only new hardware component associated with PSD operation. The memory board is identical to the memory board installed in the DCP. The indicators on the memory board, which, when illuminated, indicate when the ASOS ACU is accessing bank 1 or bank 2, are illustrated in Chapter 2, Section III and are as described in Chapter 3, Section III.